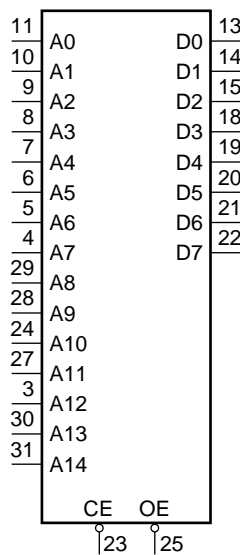
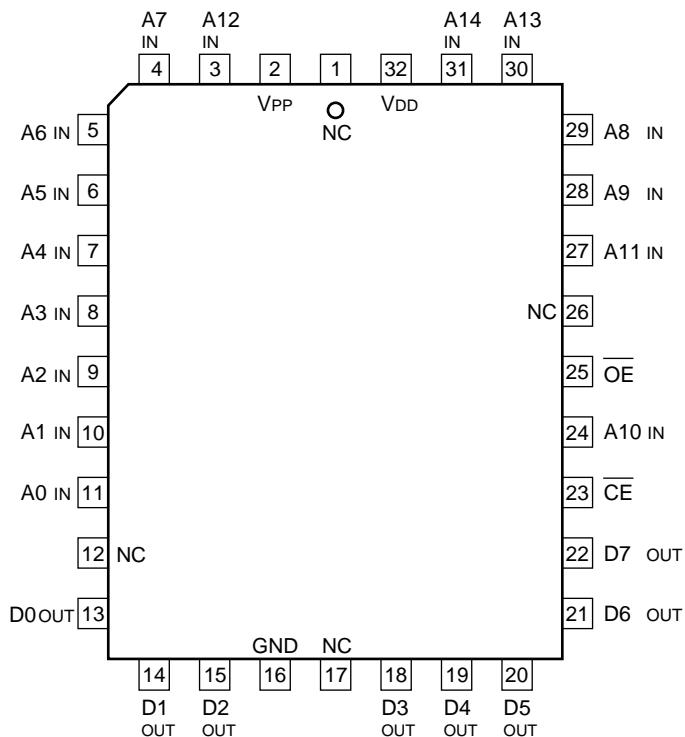


C-MOS 256K (32K × 8)-BIT EPROM

—TOP VIEW—



- A0 - A14 ; ADDRESS INPUT
- \overline{CE} ; CHIP ENABLE INPUT
- D0 - D7 ; DATA OUTPUT
- \overline{OE} ; OUTPUT ENABLE INPUT
- VPP ; PROGRAM POWER SUPPLY

An	\overline{CE}	\overline{OE}	VPP	Dn	FUNCTION
An	0	0	+5V	D out	READ
An	0	1	+5V	HI-Z	OUTPUT DISABLE
X	1	X	+5V	HI-Z	STANDBY

- 0 ; LOW LEVEL
- 1 ; HIGH LEVEL
- X ; DON'T CARE
- HI-Z ; HIGH IMPEDANCE

